LTSpice Buck Converter Simulation

ENEL373 Power Electronics

Adam Finlayson

# Simulated Circuit Diagram

Diagram, schematic

Description automatically generated

# Analytical Solutions for Inductor Sizing

## Left: Absolute Minimum L for Continuous Conduction

## Right: L for Ip-p of 0.5A

# Diagram, letter Description automatically generatedMOSFET Vgs and Vds

A picture containing chart

Description automatically generated

# Output Voltage

A picture containing chart

Description automatically generated